

WHAT IS CLAIMED IS:

1. A display panel including a plurality of signal lines, each signal line including a first portion and a second portion, the display panel comprising:

a first area including the first portions of the signal lines; and

a second area including the second portions of the signal lines,

where the first portions of the signal lines have substantially the same length, the second portions of the signal lines have different lengths, the second portion of each signal line includes a third portion and a fourth portion including at least one layer, and the number of layers in the third portion is smaller than the number of layers in the fourth portion.

2. The display panel of claim 1, wherein the length of the third portion of each signal line depends on the entire length of the signal line.

3. The display panel of claim 2, wherein the length of the third portion of relatively longer signal line is relatively shorter.

4. The display panel of claim 3, wherein the length of the third portion of each signal line is inversely proportional to the entire length of the signal line.

5. The display panel of claim 4, wherein each signal line further comprises a fifth portion for connection with outside, and the display panel further comprises a third area including the fifth portions of the signal lines and located opposite the first area with respect to the second area.

6. The display panel of claim 5, wherein the signal lines are arranged like a fan in the second area.

7. The display panel of claim 6, wherein the length of the signal line closer to edges of the second area is longer.

8. The display panel of claim 5, further comprising a driving circuit connected to the signal lines in the third area and supplying signals to the signal lines.

9. The display panel of claim 8, wherein the driving circuit is chip-mounted on the display panel.

10. The display panel of claim 8, further comprising a printed circuit mounting the driving circuit and including a plurality of conductive lines for electrical connection between the driving circuit and an external device, the conductive lines connected to the signal lines in the third area.

11. The display panel of claim 2, wherein the third portion of each signal line has a single-layered structure and the fourth portion of each signal line has a double-layered structure.

12. A display panel including a plurality of signal lines, the display panel comprising:

a first area where distances between the signal lines are substantially the same, and

a second area where distances between the signal lines vary, where each signal line includes a first portion and a second portion, the first and the second portion of each signal line are located in the second area and include at least one layer, and the number of layers in the first portion is smaller than the number of layers in the second portion.

13. The display panel of claim 12, wherein the lengths of the signal lines in the second area are different, and the length of the first portion of relatively longer signal line is relatively shorter.

14. The display panel of claim 13, further comprising a third area located opposite the first area with respect to the second area, the distances between the signal lines in the third area being shorter than the distances between the signal lines in the first area.

15. The display panel of claim 14, wherein the signal lines are arranged like a fan in the second area.

16. The display panel of claim 15, further comprising a driving circuit connected to the signal lines in the third area and supplying signals to the signal lines.

17. A liquid crystal display including a display area, first and second fan-out areas, and first and second connection areas, the liquid crystal display comprising:

an insulating substrate;

a plurality of gate lines formed on the substrate, each gate line including a connecting portion;

a plurality of data lines insulated from the gate lines and intersecting the gate lines in the display area, each data line including a connecting portion;

a gate driving circuit connected to the connecting portions of the gate lines in the first connection areas; and

a data driving circuit connected to the connecting portions of the data lines in the second connection areas,

wherein each gate line or data line include a lower portion including at least one layer and located in the first or the second fan-out area, and the number of layers in the lower portion is smaller than the number of layers in other portions.

18. The liquid crystal display of claim 17, wherein the length of the lower portion of relatively longer gate line or data line is relatively shorter.

19. The liquid crystal display of claim 18, wherein the length of the lower portion closer to a center of the driving circuits is longer.

20. The liquid crystal display of claim 17, further comprising:
a plurality of thin film transistors connected to the gate lines and the data lines; and

a plurality of pixel electrodes connected to the thin film transistors.